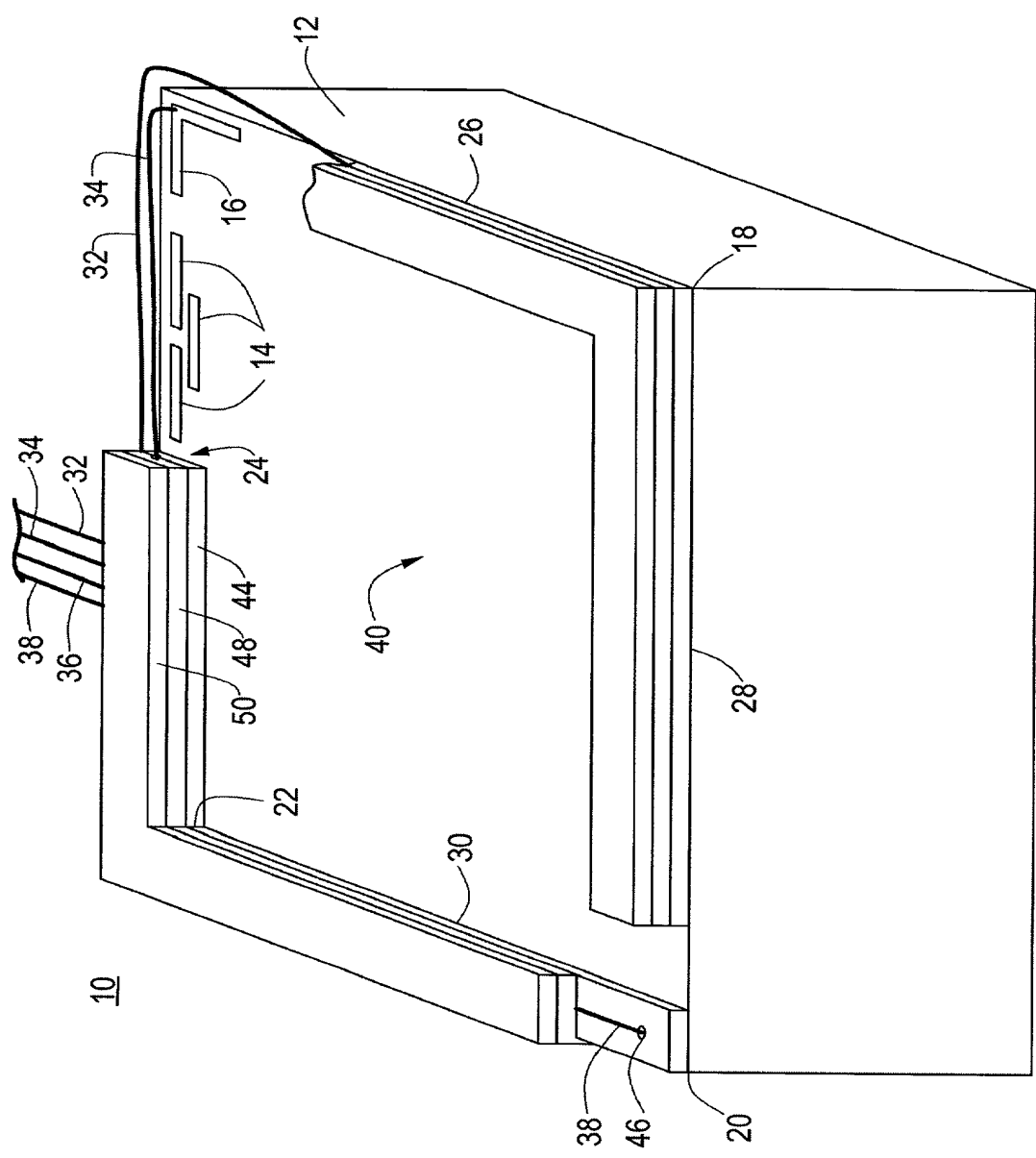


#4

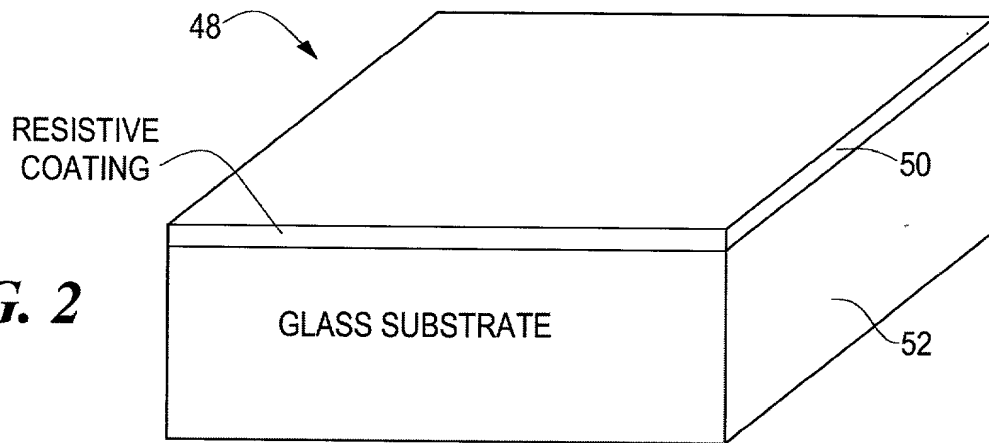
FIG. 1



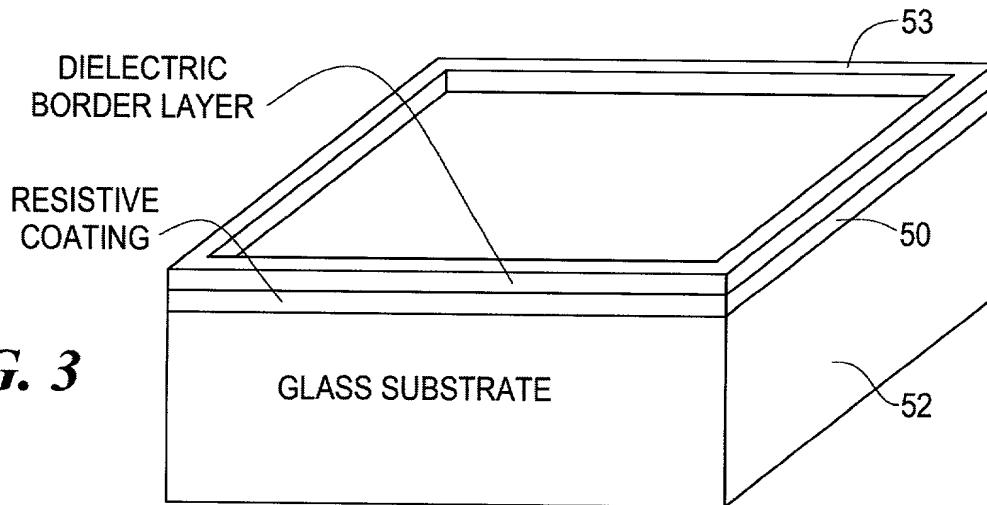
**FIG. 1**  
PRIOR ART

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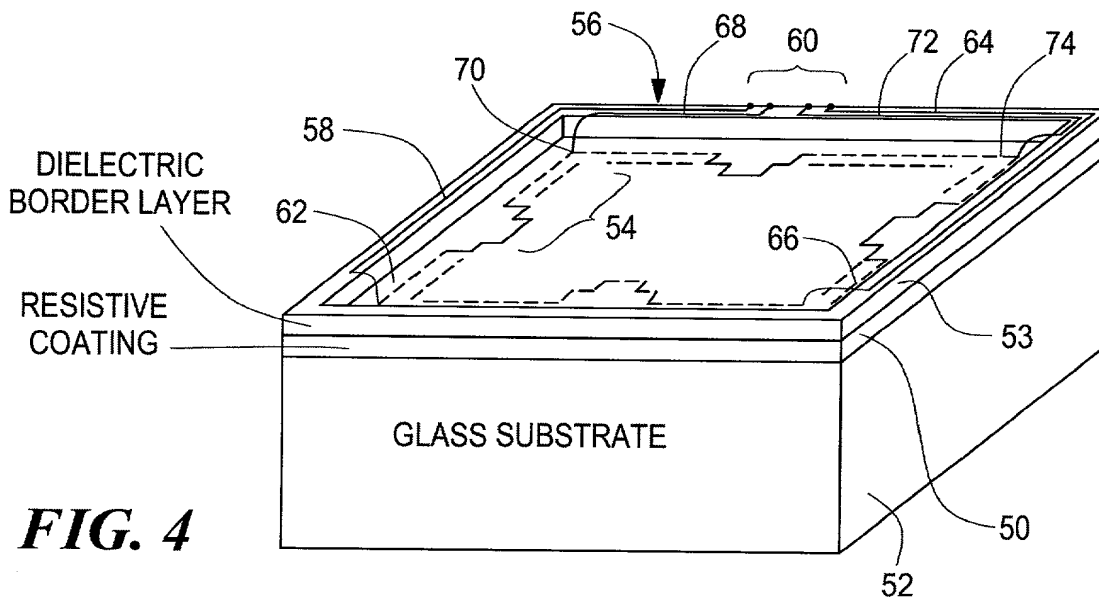
**FIG. 2**

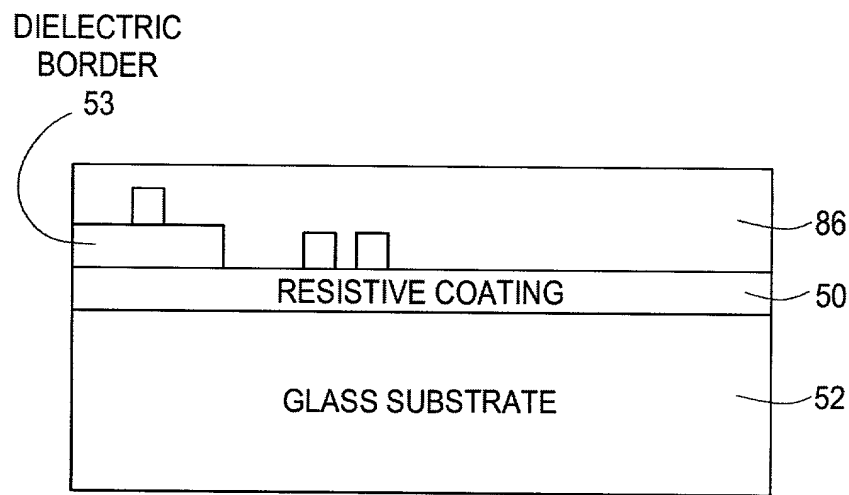


**FIG. 3**

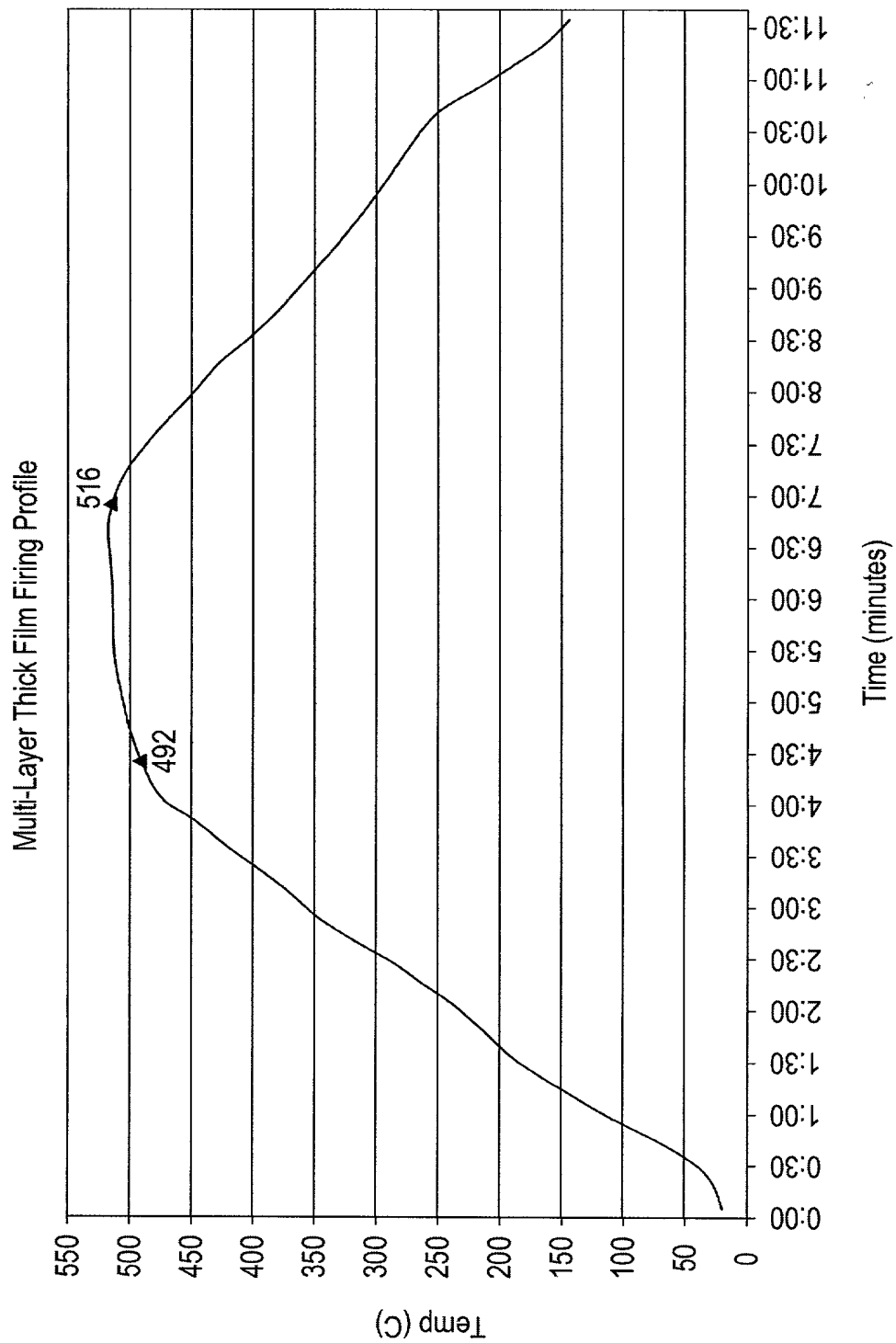


**FIG. 4**





**FIG. 5**



**FIG. 6**

**(FIG. 3)**

Apply Dielectric boarder layer  
to resistive surface (screen printing)  
and dry

100

**(FIG. 4)**

Apply wire traces on dielectric  
boarder layer and edge electrode  
pattern on resistive surface and dry  
(screen printing)

102

**(FIG. 5)**

Apply Protectic Boarder layer over  
edge electrode pattern and over  
wire traces and dielectric boarder

104

Fire  
(see Fig. 6)

106

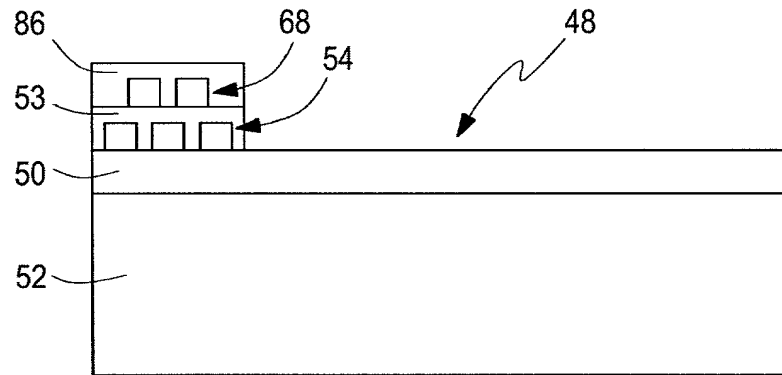
Apply Antimicrobial / Antiscratch  
Coating and/or hardcoat  
(optional)

108

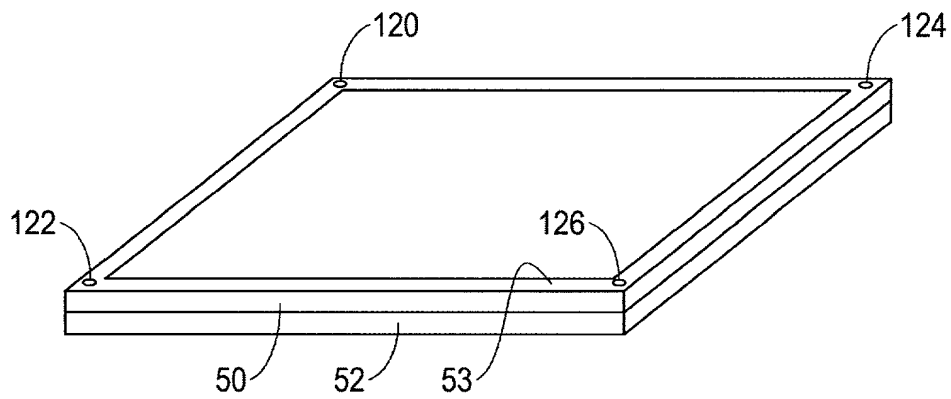
attach wiring harness

110

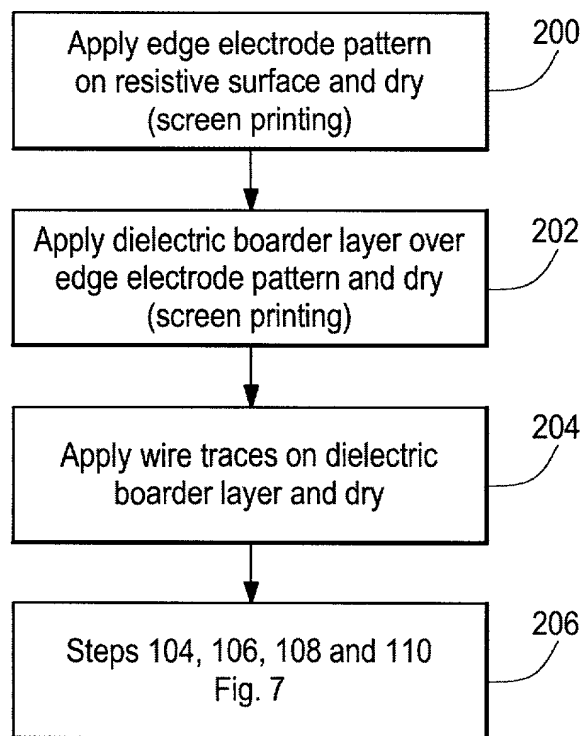
**FIG. 7**



**FIG. 8**



**FIG. 9**

**FIG. 10**